

PATENT
Serial No. 10/529,358
Amendment in Reply to Final Office Action of May 10, 2006

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended) A sigma-delta modulator, comprising:
at least two parallel filters, each receiving an input signal,
an amplifier gain device for controlling a weight associated
with an output of each of the at least two parallel filters, and
a quantizer for quantizing a weighted output from the at least
two parallel filters.

2. (Original) The sigma-delta modulator of Claim 1, wherein an
output of said quantizer is fed back as an input to said at least
two parallel filters.

3. (Previously presented) The sigma-delta modulator of Claim
1, wherein at least one of said at least two parallel filters is a

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high order filter and at least one of said at least two parallel filters is a low order filter.

4. (Currently amended) A method of sigma-delta modulation, comprising:

inputting a signal to at least two parallel filters,

using ~~a gain device~~ an amplifier for controlling a weight associated with an output of each of the at least two parallel filters, and

quantizing a weighted output from the at least two parallel filters.

5. (Original) The method of Claim 4, wherein an output of said quantizing is fed back as an input to the at least two parallel filters.

6. (Previously presented) The method of Claim 4, wherein at least one of the at least two parallel filters is a high order filter and at least one of the at least two parallel filters is a low order filter.

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7. (Previously presented) A signal processing apparatus
comprising:

an input for obtaining an input signal,
a sigma-delta modulator as claimed in Claim 1, and
an output unit for providing said output signal.